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[On-line fault detection for bus-based field programmable gatearrays](#) - [*uwe.ac.uk](#) [PDF]

NR Shridman, WH Mangione-Smith, M Potkonjak - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1998 - [ieeexplore.ieee.org](#)

... On-Line Fault Detection for Bus-Based ... line built-in self-testing (BIST) of bus-based field programmable gate arrays (FPGA's). This **system** detects deviations ...

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Fault detection on dual supply **system for a universal serial **bus system****

JB Henrie - US Patent 6,170,062, 2001 - [Google Patents](#)

... 2,2001 (54) **FAULT DETECTION ON DUAL SUPPLY SYSTEM FOR A UNIVERSAL SERIAL BUS SYSTEM**

(75) Inventor: James B. Henrie, Grayslake, IL (US) (73) Assignee: 3Com ...

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SIFT: Design and analysis of a **fault-tolerant computer for aircraft control**

JH Wensley, L Lamport, J Goldberg, MW Green, KN ... - Proceedings of the IEEE, 1978 - [ieeexplore.ieee.org](#)

... continuing at SRI on proving the correctness of the **system**. ... The unit of **fault detection** and of reconfiguration in SIFT is a processor/memory module or a **bus**. ...

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Fault detection and redundancy management **system**

PM Julich, JB Pearce - US Patent 4,634,110, 1987 - [freepatentsonline.com](#)

... which is used to coordinate the **fault detection** and redundancy ... between the processors and the **bus controller** in ... with the required speed of the **system** and the ...

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Multi-kernel flow graphs: a novel approach for **system testability analysis and **fault diagnosis****

S Deb, KR Patipati, V Raghavan, M Shakeri, R ... - Aerospace and Electronic Systems Magazine, IEEE, 1995 - [ieeexplore.ieee.org](#)

... means that the modeling approach enables the **detection** and isolation ... If, due to a **fault**, the new gain is ... see example 1 in section, "A Simple **Bus System**". ...

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Trends in the application of model-based **fault detection and diagnosis of technical processes**

R Isenmann, P Bellé - Control Engineering Practice, 1997 - [Elsevier](#)

... (2) Functions **Fault detection**: Determination of the faults present in a **system** and the time of **detection**. **Fault isolation**: Determination ...

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TNet: a reliable **system area network** - [*4z.com](#) [PDF]

RW Horst, TC Inc, CA Cupertino - Micro, IEEE, 1995 - [ieeexplore.ieee.org](#)

... Designed as an I/O **bus** replacement, the P1394 ... ability do not meet high-end **system** requirements. ... reduces pin count and improves control logic **fault detection**. ...

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FTMP—A highly reliable fault-tolerant multiprocess for aircraft

AL Hopkins Jr, TB Smith III, JH Lala - Proceedings of the IEEE, 1978 - [ieeexplore.ieee.org](#)
... in the FTMP win hdlc dl **fault detection**, dhgmsis, and ... I. INTRODUCTION T HE FTMP
(**Fault-Tolerant Multiprocessor** ... to 1965, with a serial-bus multiprocessor concept ...
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GA Kanawati, NA Kanawati, JA Abraham - Computers, IEEE Transactions on, 1995 - [ieeexplore.ieee.org](#)
... RT level RT level Device level **Bus** (RT) level ... the effectiveness of several concurrent
error detection techniques. ... these approaches is that the **fault** and error ...
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Evaluation of error detection schemes using fault injection by heavy-ion radiation

U Gunneflo, J Karlsson, J Torin - Fault-Tolerant Computing, 1989. FTCS-19. Digest of Papers., ..., 1989 - [ieeexplore.ieee.org](#)
... No existing **fault injection** method fulfills these requirements completely. ... IJ have
evaluated error **detection** schemes by ... the **data**, **address** or control **bus** of the ...
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Key authors: [J Abraham](#) - [J Dugan](#) - [G Kanawati](#) - [U Gunneflo](#) - [R Iyer](#)

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